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September 23, 2004

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Re: U.S. Patent Application No.: 10/725,827  
For: **ACTIVE MATRIX THIN FILM TRANSISTOR ARRAY BACKPLANE**  
Inventors: Forbes, Charles, *et al.*  
Filed: December 1, 2003  
Our Ref. No.: VTW-010DV1

Dear Sir:

I enclose herewith for filing in the above-identified application the following:

1. Information Disclosure Statement;
2. PTO Form SB/08;
3. A Return Postcard.

No additional costs are believed to be due in connection with the filing of this Information Disclosure Statement. However, please charge any necessary fees in connection with the enclosed statement to our Deposit Order Account No. 12-0080. For this purpose, a duplicate of this sheet is attached.

I hereby certify that this correspondence is deposited with the United States Postal Service as first class mail in an envelope addressed to:  
MS Amendment, Commissioner for Patents, P.O. Box 1450,  
Alexandria, Virginia 22313-1450 on:  
9/23/04  
Date  
Anthony A. Laurentano  
Anthony A. Laurentano, Registration No. 38,220

Respectfully submitted,  
LAHIVE & COCKFIELD, LLP

Anthony A. Laurentano  
Anthony A. Laurentano  
Registration No. 38,220  
Attorney for Applicants



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: Forbes, Charles, *et al.*

Serial No.: 10/725,827

Filed: December 1, 2003

For: *ACTIVE MATRIX THIN FILM TRANSISTOR  
ARRAY BACKPLANE*

Attorney Docket No.: VTW-010DV1

Group Art Unit: 2673

Examiner: NOT YET ASSIGNED

MS Amendment  
Commissioner for Patents  
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Alexandria, Virginia 22313-1450

Certificate of First Class Mailing (37 CFR §1.8(a))

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on the date set forth below.

9/23/04  
Date of Signature and of Mail Deposit

By:

Anthony A. Laurentano  
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Reg. No.: 38,200  
Attorney for Applicants

INFORMATION DISCLOSURE STATEMENT

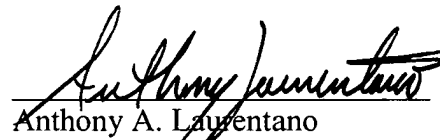
Dear Sir:

For the Examiner's convenience in reviewing this application, Applicants submit a consolidated PTO Form SB-08, listing all references cited during the prosecution of the parent applications. The present application is a continuation of U.S. Serial No. 10/300514, filed November 20, 2002 (Atty. Docket No. VTW-010). All references listed on the enclosed PTO Form SB-08 have been previously cited by or submitted to the Office in the prior application, and, in accordance with 37 CFR §1.98(d), copies of the of the references are not enclosed but will be provided upon request.

This statement is not to be interpreted as a representation that the cited publications are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such publication is prior art. Moreover, Applicants understand that the Examiner will make an independent evaluation of the cited publications.

Under 37 CFR § 1.97(b)(3), no additional costs are believed to be due in connection with the filing of this disclosure. If, however, a first Office Action on the merits issues in this application bearing a mailing date prior to the date of this Information Disclosure Statement, please charge any appropriate fee as required under 37 CFR § 1.17(p) to our Deposit Order Account No. 12-0080.

Respectfully submitted,  
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PTO/SB/08a/b (08-03)

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<b>Substitute for form 1449A/B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/725,827
				Filing Date	December 1, 2003
				First Named Inventor	Forbes, Charles
				Art Unit	2673
				Examiner Name	Not Yet Assigned
Sheet	1	of	4	Attorney Docket Number	VTW-010DV1

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	A1	4,126,854	11-21-1978	SHERIDON, N.K.	
	A2	4,143,103	03-06-1979	SHERIDON, N.K.	
	A3	4,298,448	11-03-1981	MYELLER, K., et al.	
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	A5	5,213,853	05-25-1983	NOONAN, J.M.	
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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)			
	A26	WO 98/03896 A1	01-29-1998	JACOBSON, J.M.	

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	A27	BAEUERLE, P.A., et al. "4.3: A MIM-driven transmissive display with color filters on 2-in.-diagonal plastic substrates." SID 99 Digest, pp. 414-17.	

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Substitute for form 1449A/B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/725,827
				Filing Date	December 1, 2003
				First Named Inventor	Forbes, Charles
				Art Unit	2673
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Sheet	2	of	4	Attorney Docket Number	VTW-010DV1

FOREIGN PATENT DOCUMENTS						
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
	B1	WO 98/41898 A2	09-24-1998	JACOBSON, J.M., et al.		
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	B4	WO 00/26761 A1	05-11-2000	ALBERT, J.D., et al.		
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	B17	WO 01/08242 A1	02-01-2001	AMUNDSON, K., et al.		
	B18	WO 01/17029 A1	03-08-2001	AMUNDSON, K., et al.		
	B19	WO 01/17040 A1	03-08-2001	AMUNDSON, K., et al.		
	B20	WO 01/17041 A1	03-08-2001	AMUNDSON, K., et al.		
	B21	WO 01/33621 A2	05-10-2001	SMITH, J.S.		
	B22	WO 01/80287 A2	10-25-2001	JACOBSON, J.M., et al.		

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	B23	CHEN, et al. "12.2: A conformable electronic ink display using a foil-based a-Si TFT array." SID 01 Digest, pp. 157-159.	
	B24	COMISKEY, et al. "An electrophoretic ink for all-printed reflective electronic displays." Nature, 1998; vol. 394, pp. 253-255.	
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	B27	GLESKOVA, et al. "Electrophotographic patterning of thin-film silicon on glass foil." IEEE Electron Device Letters, 1995; vol. 16(10), pp. 418-420.	
	B28	GLESKOVA, et al. "Electrophotographic patterning of a-Si:H." AMLCDs 1995 Workshop Proceedings, 1995; pp. 16-19.	
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				Filing Date	December 1, 2003
				First Named Inventor	Forbes, Charles
				Art Unit	2673
				Examiner Name	Not Yet Assigned
Sheet	3	of	4	Attorney Docket Number	VTW-010DV1

C1	GLESKOVA, <i>et al.</i> "a Si:H TFT fabricated by electrophotographic printing." Display Manufacturing Technology Conference. Digest of Technical Papers. 1996; pp. 97-98.	
C2	GLESKOVA, <i>et al.</i> "a Si:H TFTs patterned using laser-printed toner." Mat. Res. Soc. Symp. Proc., 1997; vol. 424, pp. 71-76.	
C3	GLESKOVA, <i>et al.</i> "a Si:H TFTs made on polyimide foil by pe-cvd at 150°C." Mat. Res. Soc. Symp. Proc., 1998; vol. 508, pp. 73-78.	
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C5	GLESKOVA, <i>et al.</i> "Rugged a-Si:H TFTs on plastic substrates." Mat. Res. Soc. Symp. Proc., 1999; vol. 557, pp. 653-658.	
C6	GLESKOVA, <i>et al.</i> "Amorphous silicon thin-film transistors on compliant polyimide foil substrates." IEEE Electron Device Letters, 1999; vol. 20(9), pp. 473-475.	
C7	GLESKOVA, <i>et al.</i> "Failure resistance of amorphous silicon transistors under extreme in-plane strain." Applied Physics Letters, 1999; vol. 75(19), pp. 3011-3013.	
C8	GLESKOVA, <i>et al.</i> "a-Si:H thin-film transistors after very high strain." Journal of Non-Crystalline Solids, 2000; vol. 266-269, pp. 1320-1324.	
C9	GLESKOVA, <i>et al.</i> "a Si:H TFTs on polyimide foil: Electrical performance under mechanical strain." Asia Display/IDW '01, 2001; pp. 331-334.	
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C11	GLESKOVA, <i>et al.</i> "Electrical stability of a-Si:H TFTs fabricated at 150°C." Res. Soc. Symp. Proc., 2001; vol. 664, pp. A19.7.1-A19.7.6.	
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C14	GLESKOVA, <i>et al.</i> "Electrophotographically printed insulator." Materials Letters, 2002; vol. 52, pp. 150-153.	
C15	GLESKOVA, <i>et al.</i> "Electrical response of amorphous silicon thin-film transistors under mechanical strain." Journal of Applied Physics, 2002; vol. 92(10), pp. 6224-6229.	
C16	GLESKOVA, <i>et al.</i> "Effects of mechanical strain on amorphous silicon thin-film transistors." Mat. Res. Soc. Symp. Proc., 2002; vol. 715, pp. 667-677.	
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C19	SAZONOV, <i>et al.</i> "Fabrication of a-Si:H TFTs at 120°C on flexible polyimide substrates." Mat. Res. Soc. Symp. Proc., 2000; vol. 558, pp. 375-381.	
C20	SHEN, <i>et al.</i> "Patterning of a-Si:H by laser printing." SID Digest of Technical Papers, 1995; pp. 587-590.	
C21	STURM, <i>et al.</i> "Three-dimensional electronic surfaces." Mat. Res. Soc. Symp. Proc., 2001; vol. 636, pp. D11.4.1-D11.4.12.	
C22	STURM, <i>et al.</i> "Enabling technologies for plastic displays." Proc. SPIE, 2002; vol. 4712, pp. 222-236.	
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C26	WAGNER, <i>et al.</i> "Compliant substrates for thin-film transistor backplanes." In <u>Flat panel display technology and display metrology</u> . Proceedings of SPIE, 1999; vol. 3636, pp. 32-39.	
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Examiner Signature		Date Considered	
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				Art Unit	2673
				Examiner Name	Not Yet Assigned
Sheet	4	of	4	Attorney Docket Number	VTW-010DV1

	D1	WAGNER, <i>et al.</i> "Novel processing technology for macroelectronics." In <u>Technology and applications of amorphous silicon</u> , Street (ed.) Springer, 2000, pp. 222-251.	
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